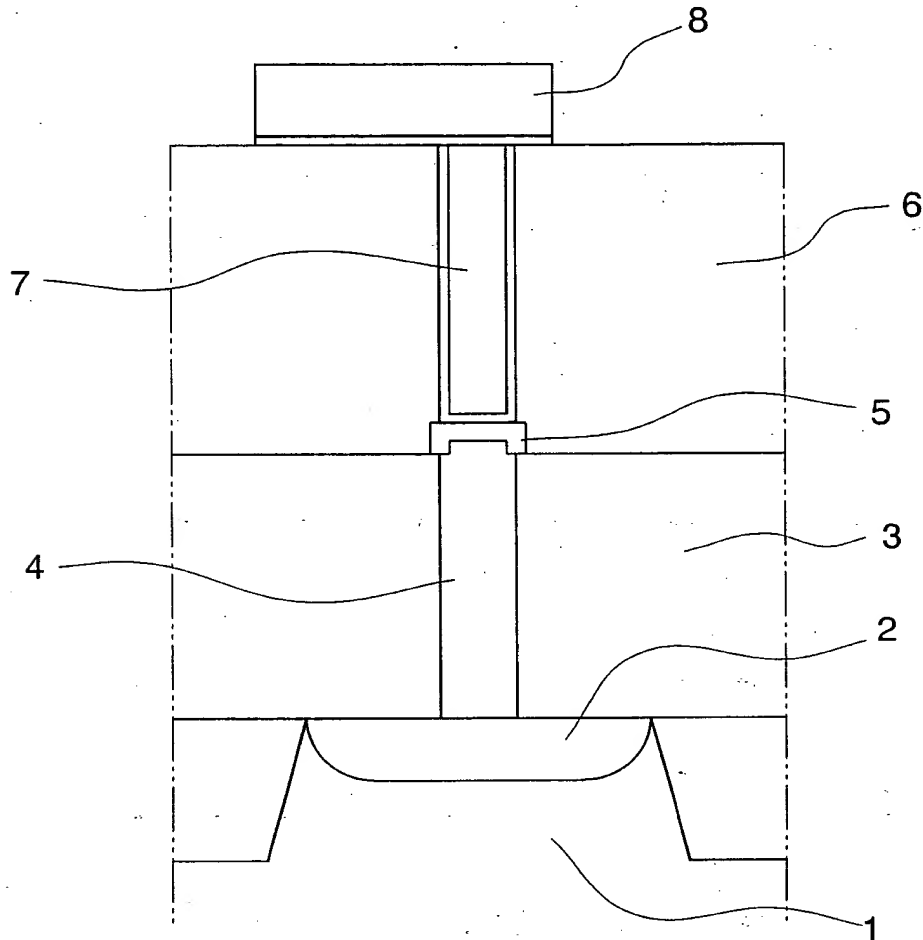




Fig. 1



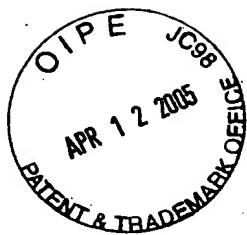


Fig. 2A

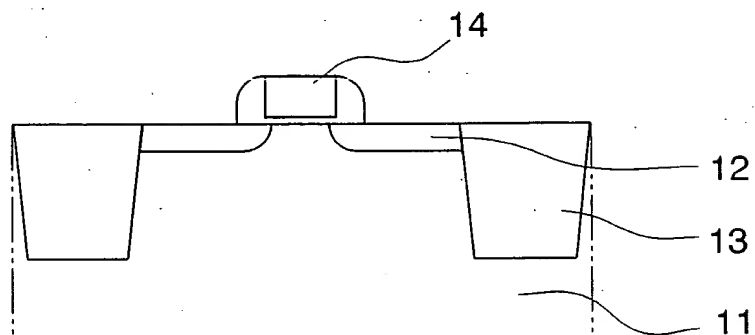


Fig. 2B

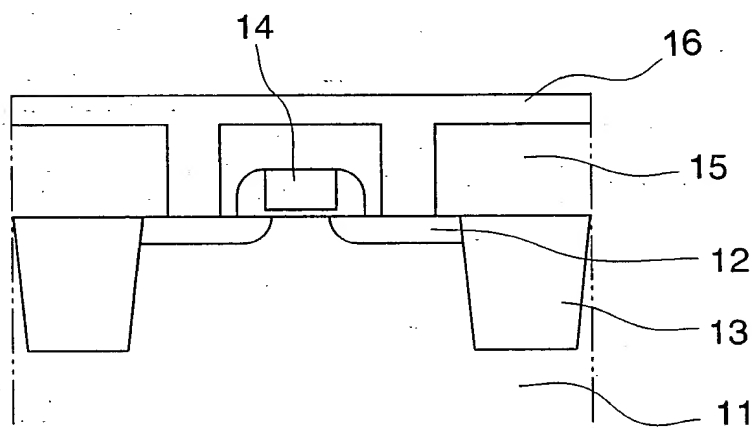


Fig. 2C

SURFACE LEVEL OF
FIRST INSULATING FILM
AFTER IT IS ETCHED

SURFACE LEVEL OF FIRST
INTERLAYER INSULATING FILM AFTER
POLYSILICON IS ETCHED BACK

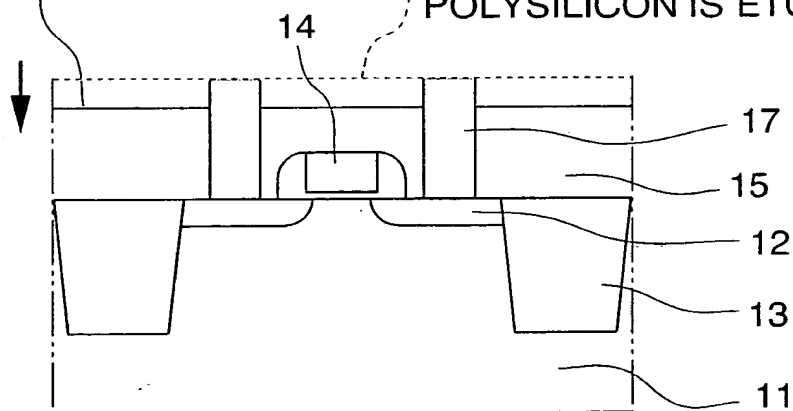




Fig. 3A

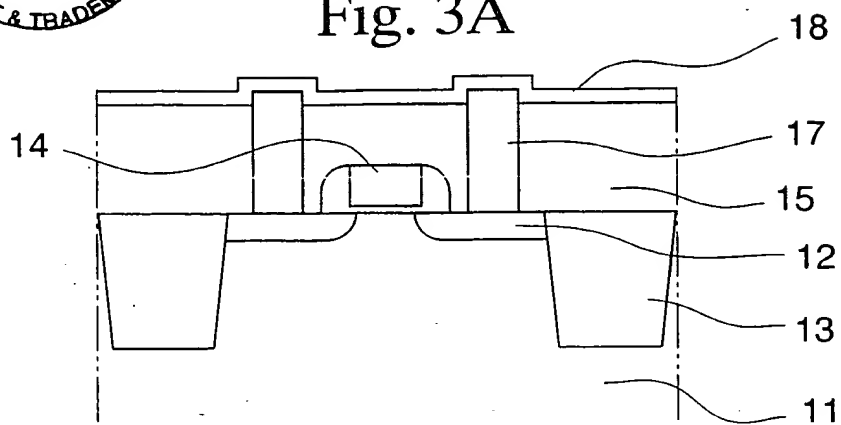


Fig. 3B

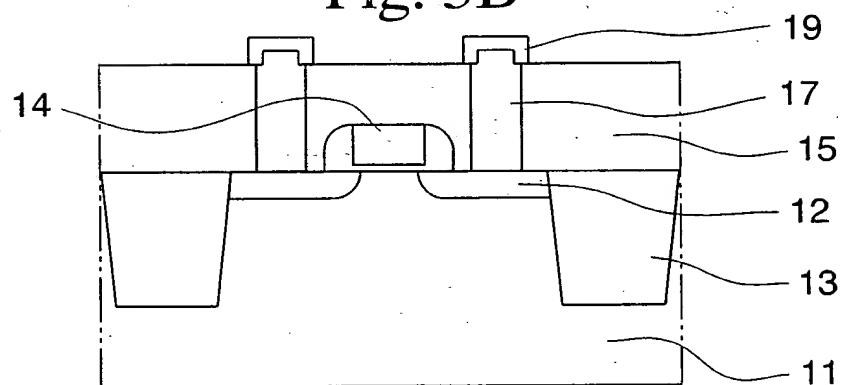


Fig. 3C

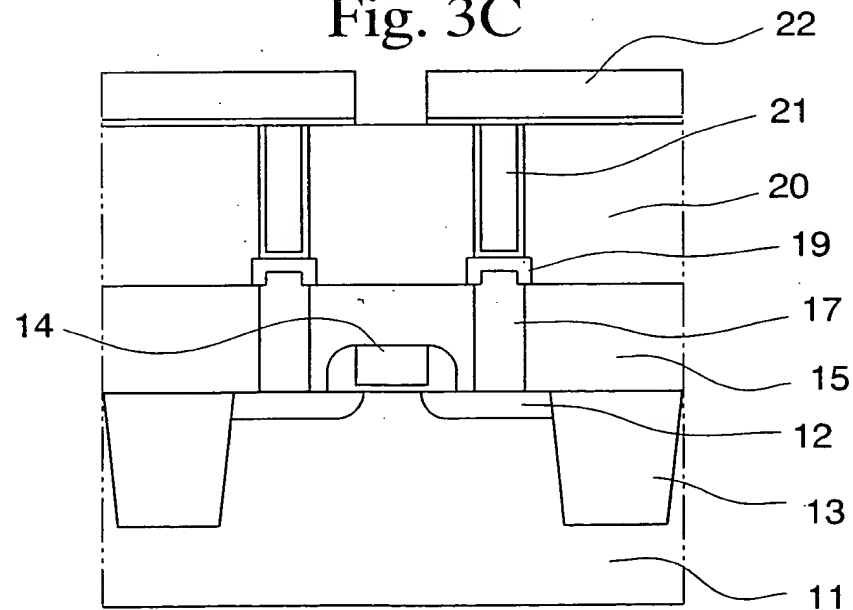




Fig. 4

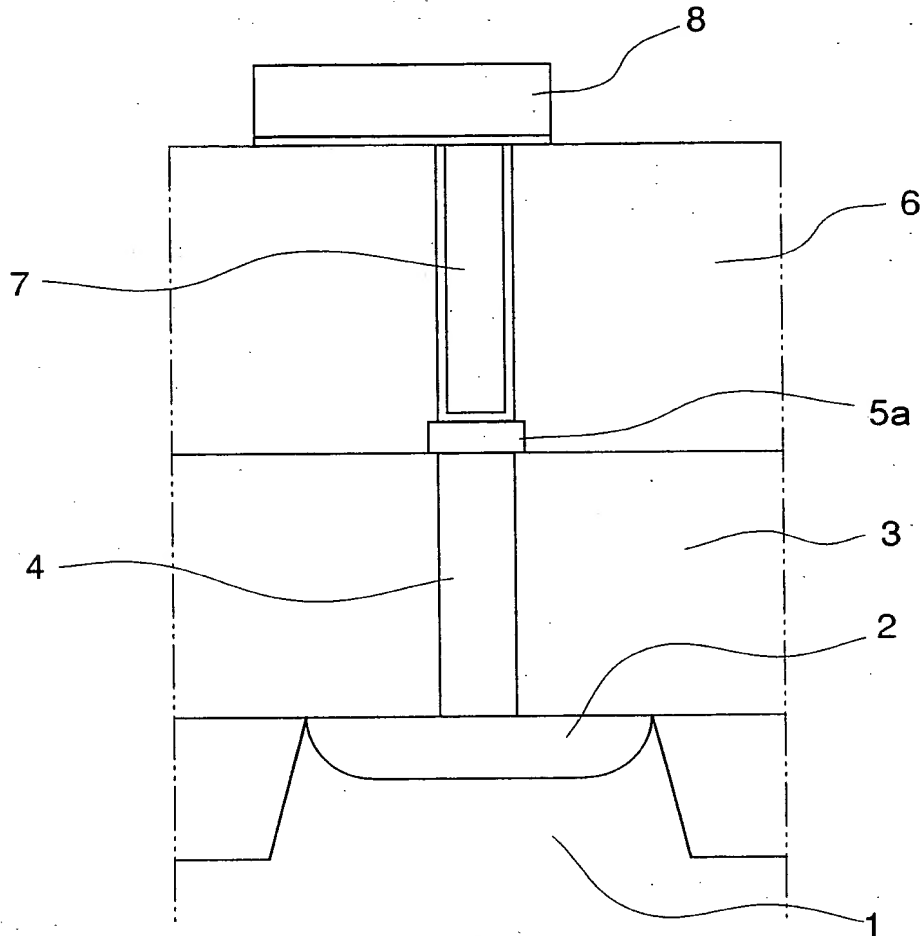




Fig. 5A (PRIOR ART)

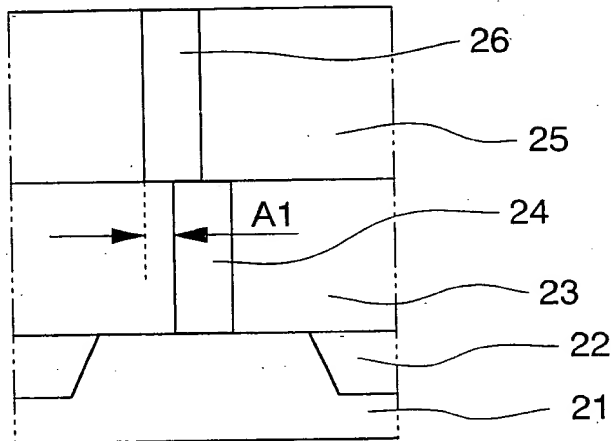


Fig. 5B (PRIOR ART)

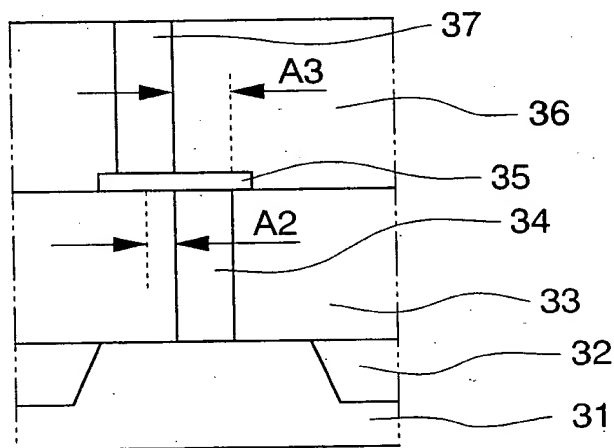


Fig. 5C (PRIOR ART)

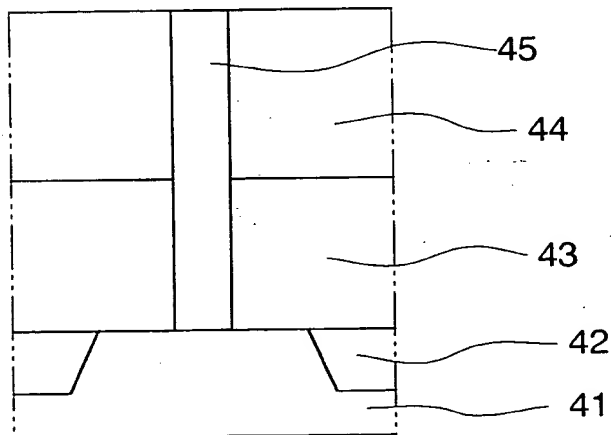




Fig. 6 (PRIOR ART)

